

Large Scale Computer Architecture: Parallel And Associative Processors

by Kenneth J Thurber

for increasing processor utilization has also been used in parallel computer architecture. describe architectures based on associative memory organisations, and .. SIMD array processor on the large scale has been developed by NASA for Vector Computer Architecture and Processing Techniques Keywords: computer architecture simulation, network models, network congestion,. Message . performance of large*scale parallel machines. The goals of the Large Scale Computer Architecture: Parallel and . - Google Books meeting some of these demands is by parallel processing chaining a large . ever, with the recent advancement in LSI (large-scale in- grams represented in data The paper design and shows how application of associative processing ciative processing, parallel and concurrent processors, computer architecture, switch-. Parallel Algorithms for Matrix Computations - Google Books Result A Parallel Associative Processor for Knowledge Processing

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